



Static Code Analysis for C++ Applications

C++ Core Meet up 28-12-2017

- Short Introduction, who am I, from where?
- What is Static Code Analysis?
- Pattern matching SCA vs Data Flow based SCA
- Short demo of Analysis, pattern matching and Data Flow

אי. אס. אל. מערכות תוכנה בע"מ

מרכז מומחיות לכל האספקטים של ניתוח קוד סטטי ובדיקות דינאמיות – שרות מלא
מקצה לקצה

- יעוץ מקצועי, התקנה והדרכה קורסים.
- אינטגרציה לתוך מערך הבדיקות והפיתוח בארגון
- פיתוח מערכי בדיקות ובדיקות ייעודיות
- יעוץ בכתיבת קוד נכון ותיקון בפועל (או יעוץ לדרכי תיקון מעשיות) של טעויות שהתגלו על ידי כלי הבדיקה.
- הוקמה ב 2005 ממוקמת ברמת גן , ומעסיקה 9 עובדים.
- מייצגת את חברת Parasoft INC מאז שנת 2007
- 170 לקוחות פאראסופט בישראל
- לקוחות עיקריים – חברות ציוד רפואי (Covidien , Philips and GE Medical), פיננסים (בורסת ת"א, בנק דיסקונט), התעשייה הביטחונית (תעשייה אווירית, אלביט), מגזר השבבים (Broadcom , Marvell) ;

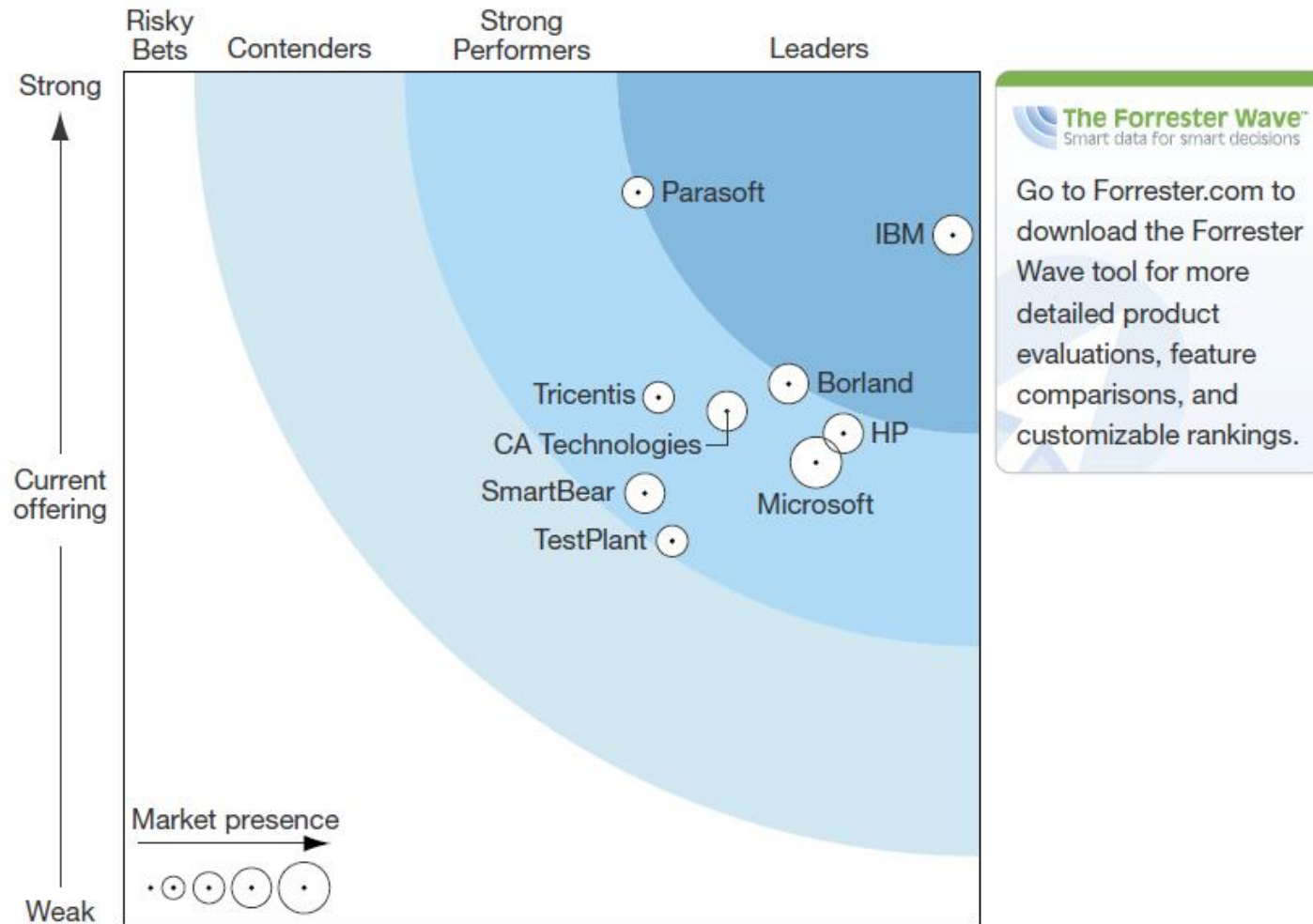
Parasoft Company Background











- Founded in 1987, privately held
- Founder and CEO until 2012 Dr. Adam Kolawa from CalTech
- Headquarters in Monrovia, CA
- 22 locations and 500+ employees worldwide
- 80 million LOC VS 290 Developers
- Analyst Technical Innovator
- 53 US patents for software technology
- 17,000+ customers worldwide
- 85% Fortune 100 Companies



Figure 4 Forrester Wave™: Modern Application Functional Test Automation, Q2 '15



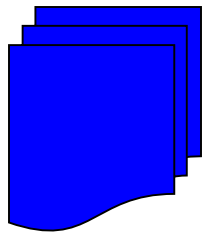
	<ul style="list-style-type: none"> • Coding Standards enforcement • Automatic Unit testing • Embedded Support • Security Testing 	<ul style="list-style-type: none"> • Data Flow analysis • Auto Stub generation • Regression Test • Code /Test Coverage
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	<p>Automated Runtime Memory Defect Detection for C/C++</p>	
	<ul style="list-style-type: none"> • End to End Testing • Web Application Testing • Functional/Integration Testing • Application Behavior Virtualization 	<ul style="list-style-type: none"> • Security Testing • Regression Testing • Policy Enforcement • Load Testing
	<ul style="list-style-type: none"> • Application Behavior Virtualization • Development/test environment management 	
	<p>Ensures the security reliability and performance of enterprise-grade mobile applications.</p>	
	<p>Decision Support Mechanism- Visibility, Control and Management of SDLC</p>	

What is it “Static Code Analysis” and what is it used for?

Static program analysis is the analysis of computer software that is performed without actually executing programs.

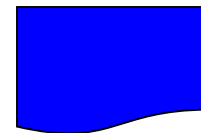
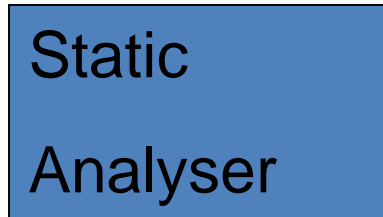
In most cases the analysis is performed on some version of the source code and in the other cases some form of the object code. ...

Static analyzers: General form



Document

Eg. Source code: .C.CPP .H .CC ,CS etc..



- Syntax violation
- Coding Standards Deviation
- Data flow info
- Control flow info
- Defects
- Errors
- Bugs

Static Code Analysis is an Automatic code review tool!

Usually performed during coding (**recommended**) or *after* the coding finished (after compilation, after integration build)

Serves same goals as code review

- Excellent for enforcing compliance to standards
- Helps to eliminate *certain* bugs
- Helps to identify *certain* design/implementation flaws
- Provides *certain* educational value

In simple words.....

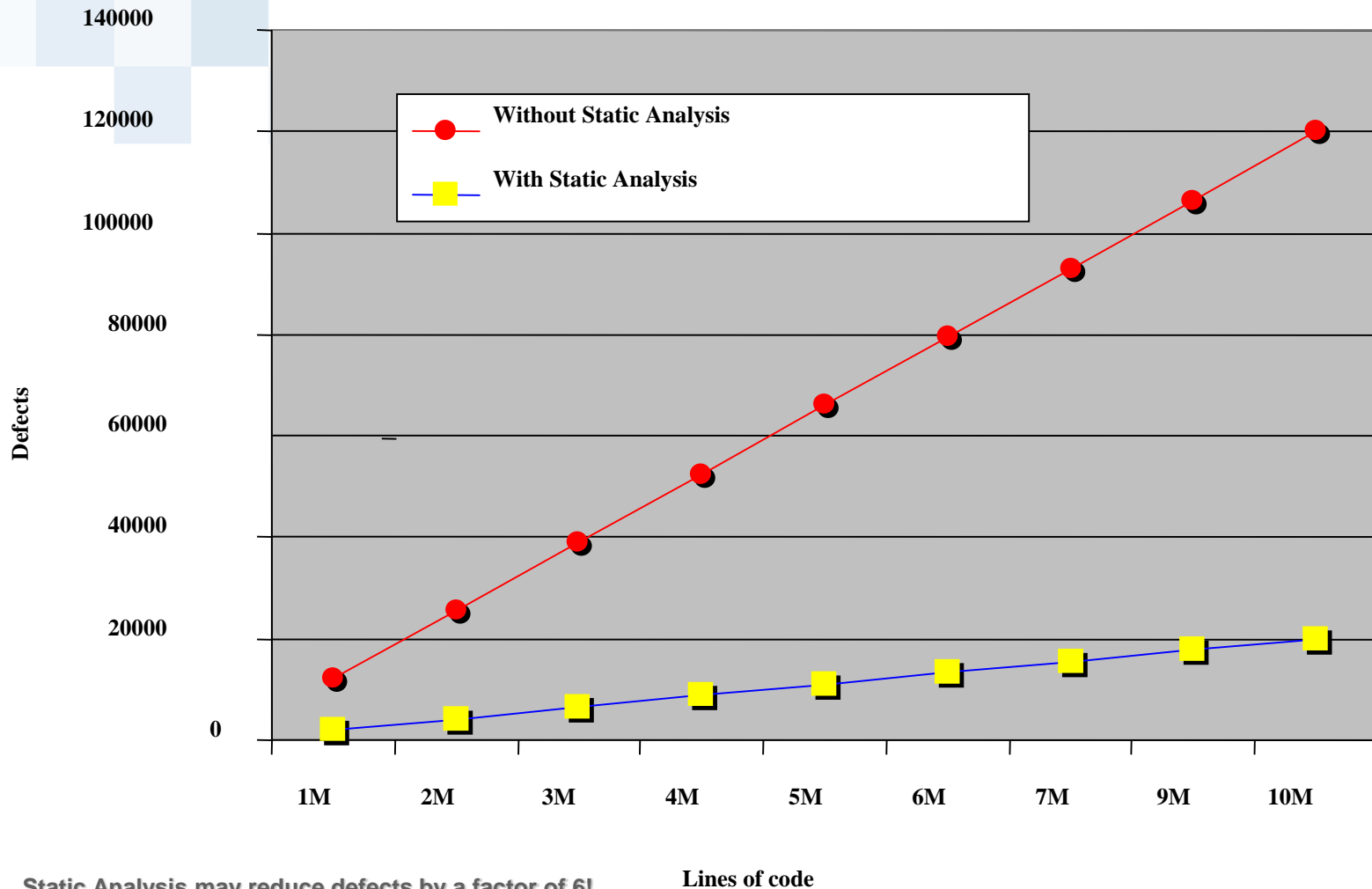
- It is an advanced and easy to use “debug tool”
- It is a Coding Standards Enforcer
-
- It is a programmers training tool
- It is the only viable way to expose Data Flow related coding defects
- It is a modern and automated way of doing what we always did manually for finding bugs and ensuring source code quality.....



AllPosters



Impact (benefit) of Static Analysis is high



Source: Capers Jones, Software Productivity Group, Inc.

- ***Pattern-Based Static Analysis***
 - Increases productivity by preventing errors
 - Extensive breadth of rules
 - 2300 for C/ C++
 - Over 1,000 for Java
 - Over 700 for .NET
 - Parasoft Test rule quality based on over 20 years of research
 - **No false positives / No False Negative**
 - Depth of analysis
 - Graphical interface for custom rule creation and customization
 - Extensive security Rulesets for (PCI, OWASP, Sun Java Security...
- ***Flow-Based Static Analysis***
 - Finds bugs
 - Deep, multi-file path analysis
 - **Very low false positives**
- ***Metrics Analysis***
 - Finds complex code prone to errors
 - Directly pinpoints areas of code/application prone to errors
 - Large breadth of metrics available

- **Prefer lambdas over `std::bind`, `std::bind1st` and `std::bind2nd`**
[CODSTA-MCPP-07-2]
- Scott Meyers, "Effective Modern C++, 42 specific ways to improve your use of C++11 and C++14", O'Reilly Media, Inc., Copyright 2015, Chapter 6: "Lambda Expressions", Item 34: "Prefer lambdas to `std::bind`"
(Since C++17, `std::bind1st` and `std::bind2nd` are removed from the Standard)

Why?

This rule detects when '`std::bind`', '`std::bind1st`' or '`std::bind2nd`' are used in code.

Older versions of the Standard used '`std::bind`', '`std::bind1st`' or '`std::bind2nd`'. C++11 allows you to use lambda expressions that are more readable, more expressive and make your code easier to optimize. In C++11, lambda expressions cannot replace polymorphic function objects and they do not offer move capture. However, C++14 introduces polymorphic lambda expressions, as well as generalized lambda capture, which enables you to replace '`bind`' in all cases.

EXAMPLE

```
#include <functional>

int f_a(int a, int b);

template <typename T> void ft(T t)
{
    int a;
    t(a);
}

void foo( void )
{
    auto fn = std::bind(f_a, 10, std::placeholders::_1); // Violation

    int a = 10;
    ft(std::bind(f_a, a, std::placeholders::_1)); // Violation
}
```

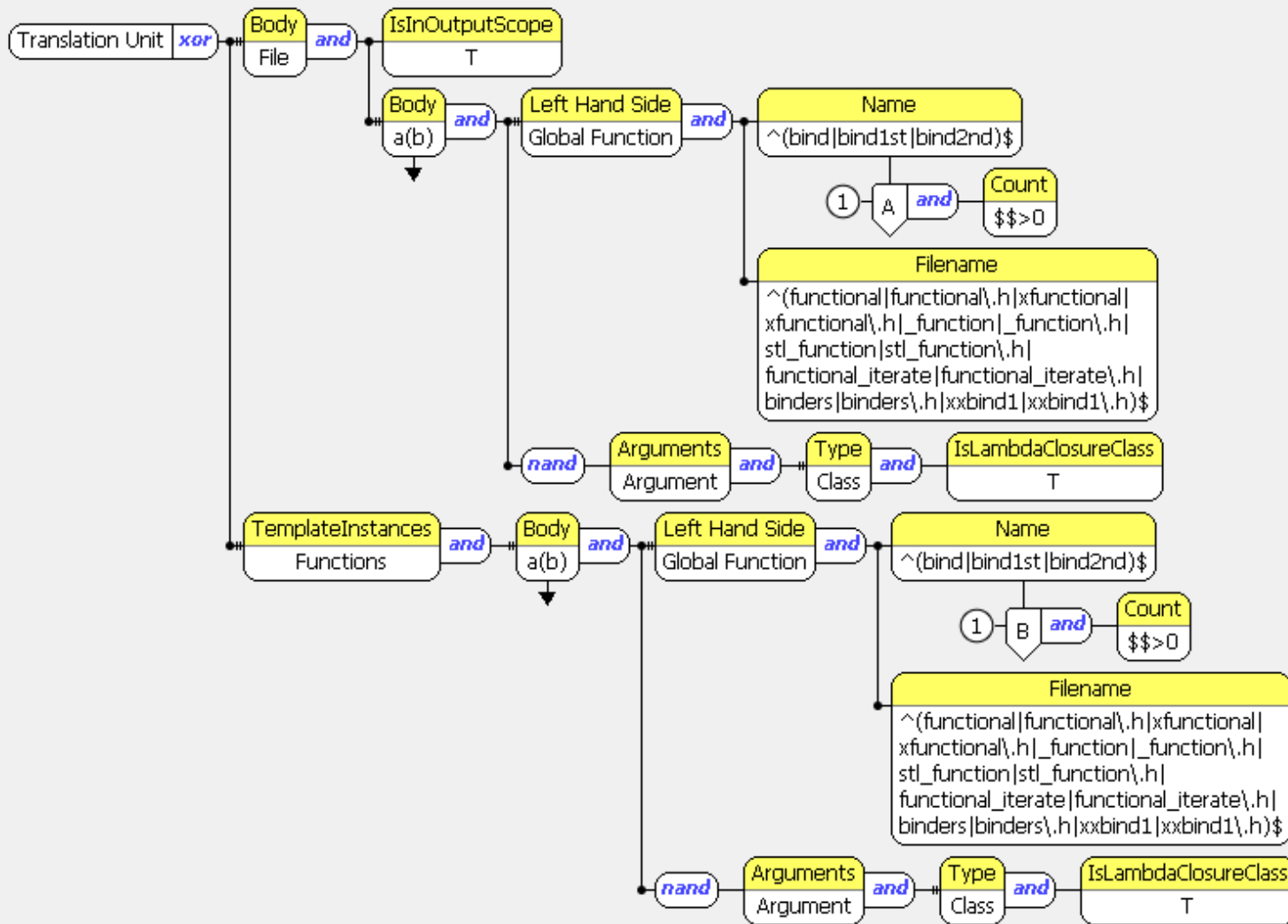
Exceptions to the rule?



EXCEPTIONS

The rule does not report a violation when a lambda is passed to 'std::bind' as an argument. This may happen when move capture is not available(in C++11). For example:

```
std::bind([] (int a) {}, 10);
```

REPAIR

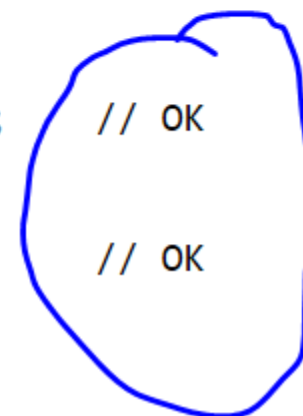
```
#include <functional>

int f_a(int a, int b);

template <typename T> void ft(T t)
{
    int a;
    t(a);
}

void foo( void )
{
    auto l_f1 = [](int a){ return f_a(10, a); }; // OK

    int a = 10;
    ft([a](int b){ return f_a(a, b); }); // OK
}
```



- There is One thing the Code Review hardly can do....
- Inter procedural Crash Causing Defects

the Solution:

Data Flow Analysis

What Can be found with Data Flow Analysis ?

- Null pointer dereference
- Use after free
- Double free
- Array indexing errors
- Mismatched array new/delete
- Potential stack overrun
- Potential heap overrun
- Return pointers to local variables
- Logically inconsistent code
- Uninitialized variables
- Invalid use of negative values
- passing large parameters by value
- Under allocations of dynamic data
- Memory leaks
- File handle leaks
- Network resource leaks
- Unused values
- Unhandled return codes



C++test – Bug Detective Data Flow Analysis

How does it work?

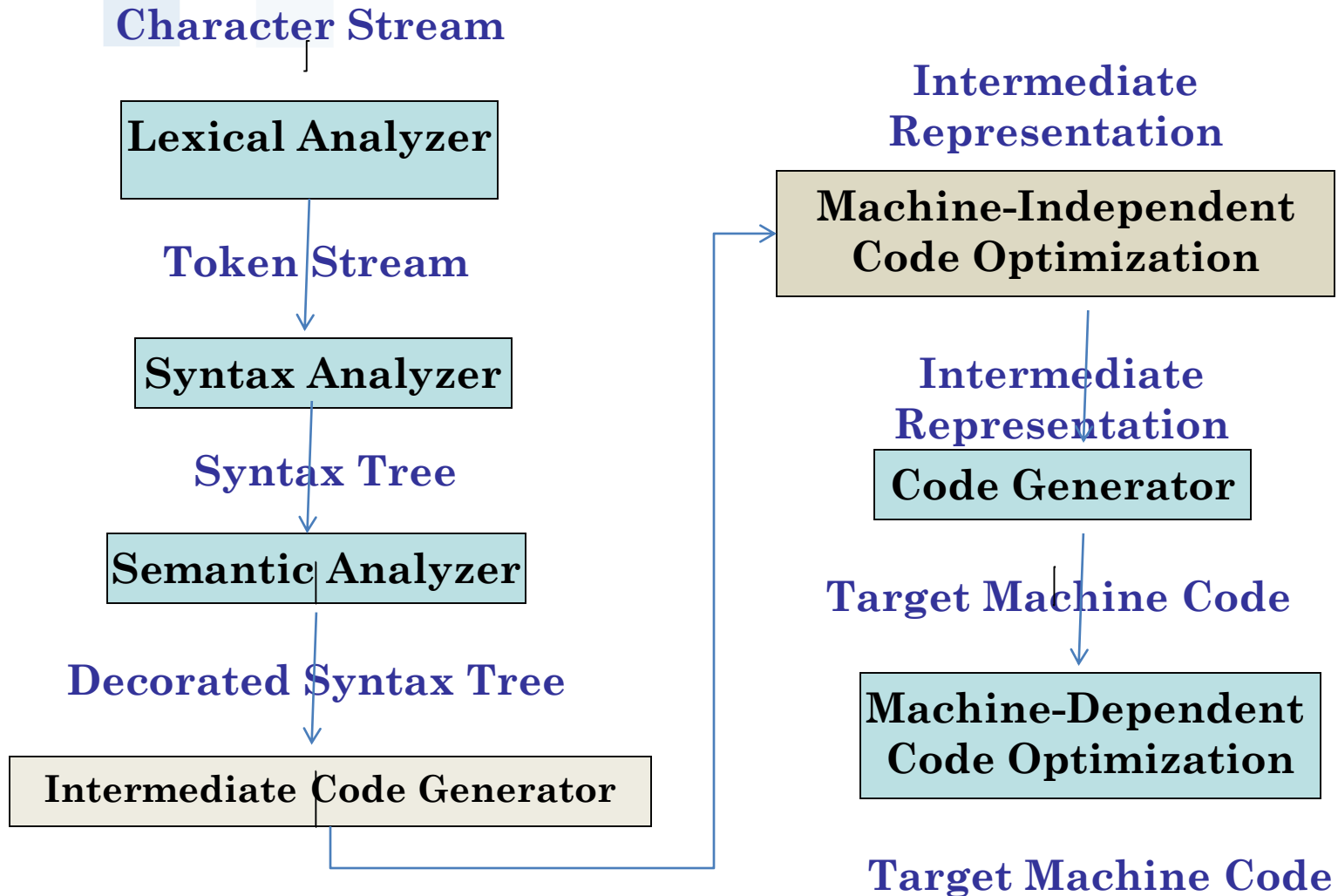
source code

```
int a, b;  
  a = 2;  
b = a*2 + 1;
```

target code

```
SET    R1,2  
STORE #0,R1  
SHIFT R1,1  
STORE #1,R1  
ADD    R1,1  
STORE #2,R1
```

Compiler components



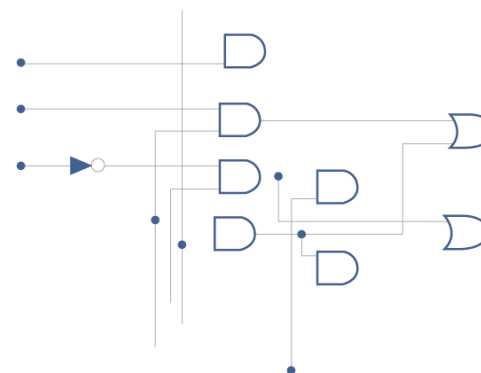
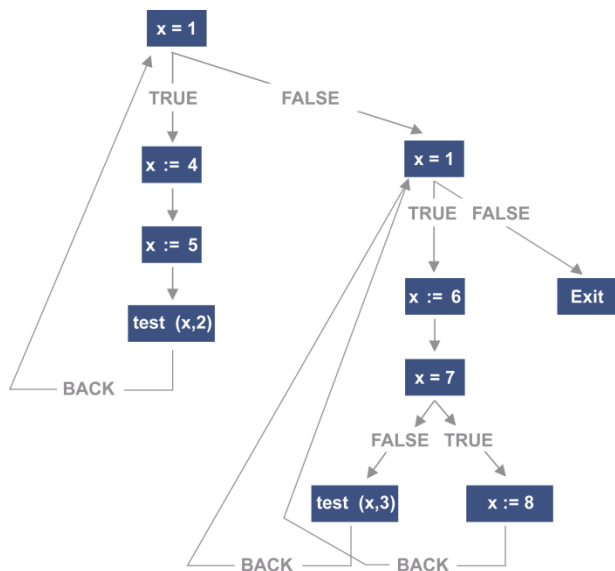
- An accurate representation of a software system based on understanding all operations that the build system performs as well as an authentic compilation of every source file in that build system.
- Software DNA Map enables static code analysis to overcome its previous limitations of excessive false positives and deliver accurate results that developers can put to immediate use.

- Bit-accurate representation of the data and logic of the software system allows SAT solvers to explore all possible values
- Enables integer overflow detection and optimal false path pruning

Control Flow



Bit-Accurate Representation



▶ = NOT

D = AND

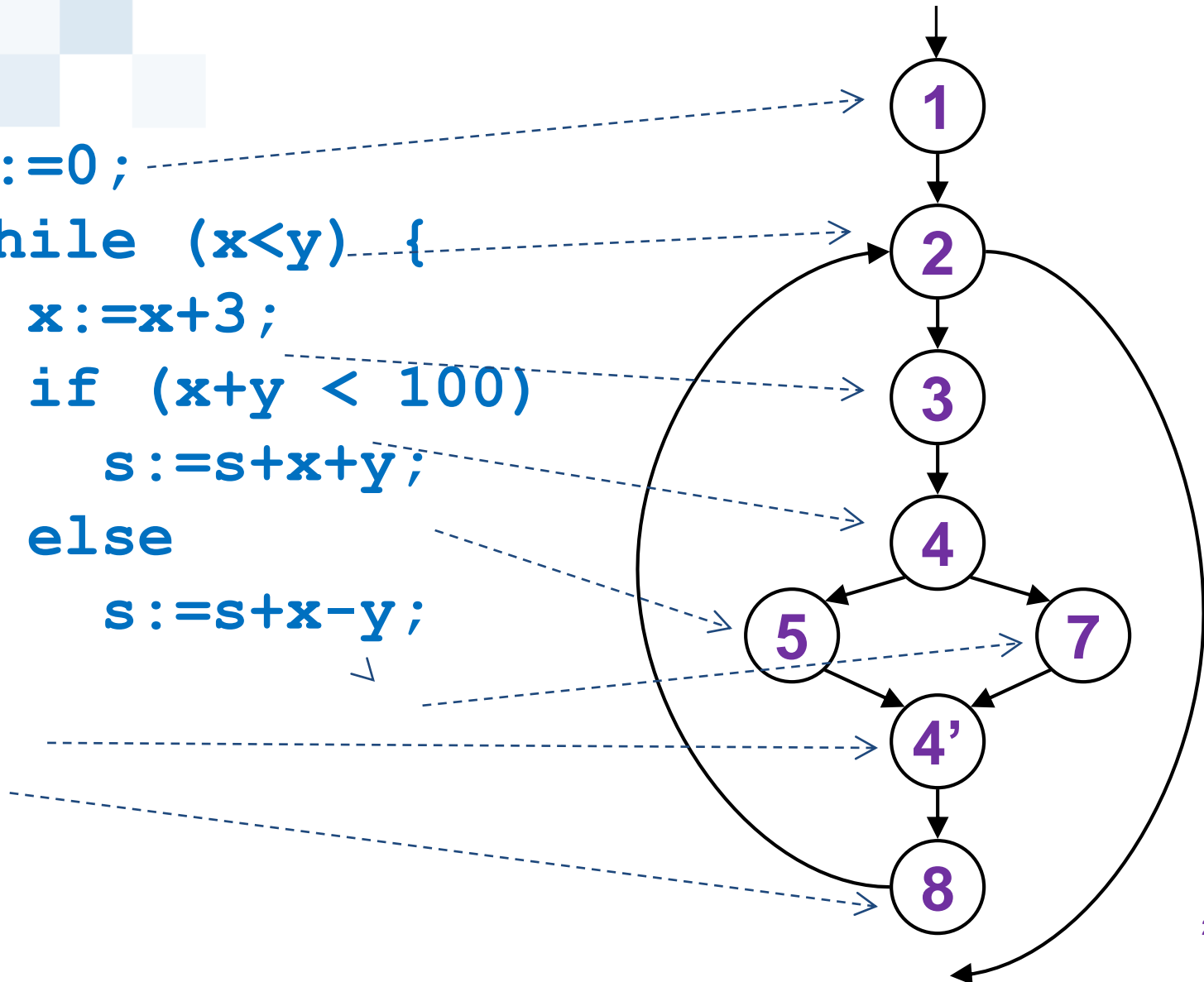
D = OR

Example of a Control Flow Graph

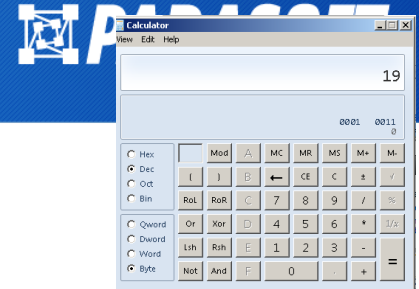
```

1. d:=0;
2. while (x<y) {
3.   x:=x+3;
4.   if (x+y < 100)
5.     s:=s+x+y;
6.   else
7.     s:=s+x-y;
8. }

```



Boolean Satisfiability (SAT Solver) using the DNA map



- Take the expression $A == 19$ (A is a 8 bit char) ,
- DNA mapping will convert it to :

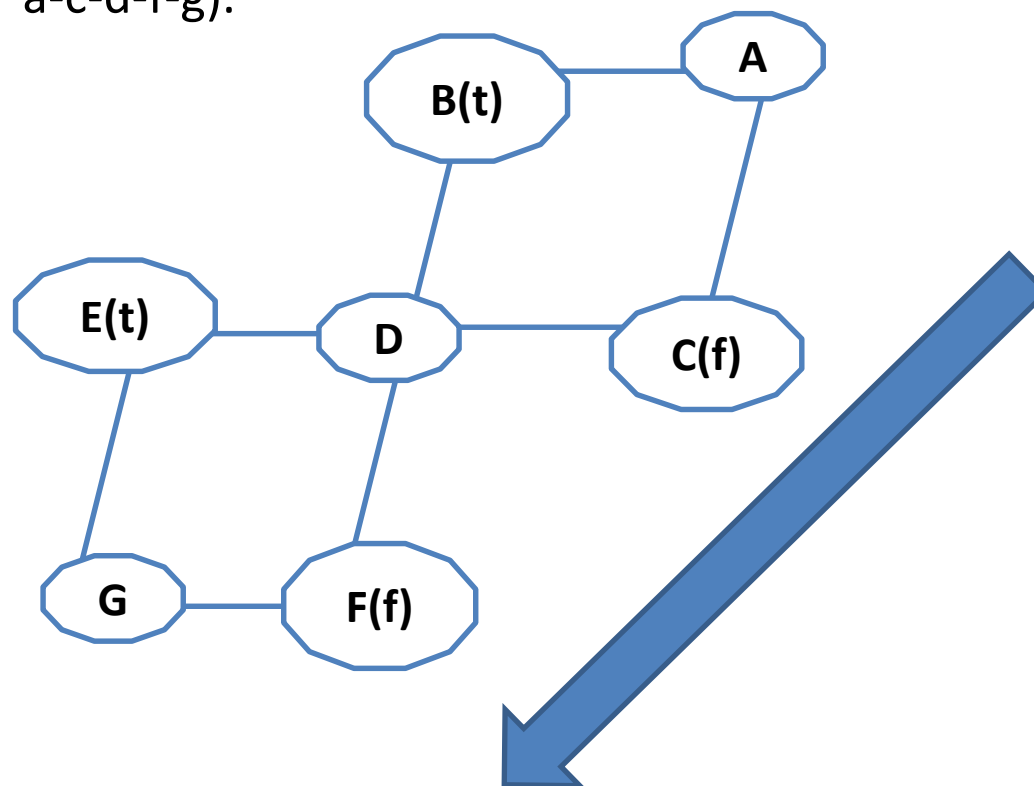
$!a7 \wedge !a6 \wedge !a5 \wedge a4 \wedge !a3 \wedge !a2 \wedge a1 \wedge a0$

(a7 is the high bit)

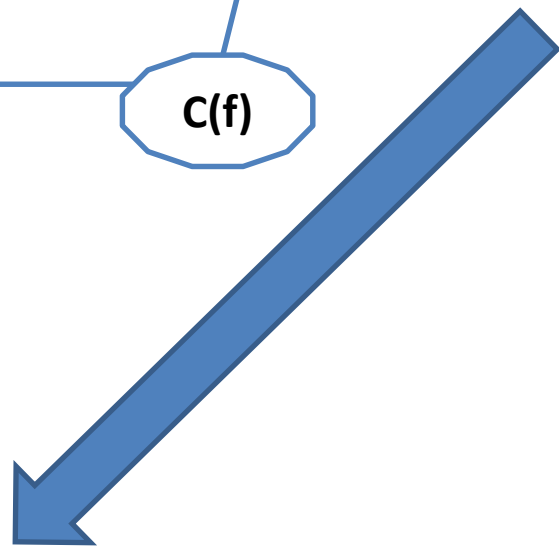
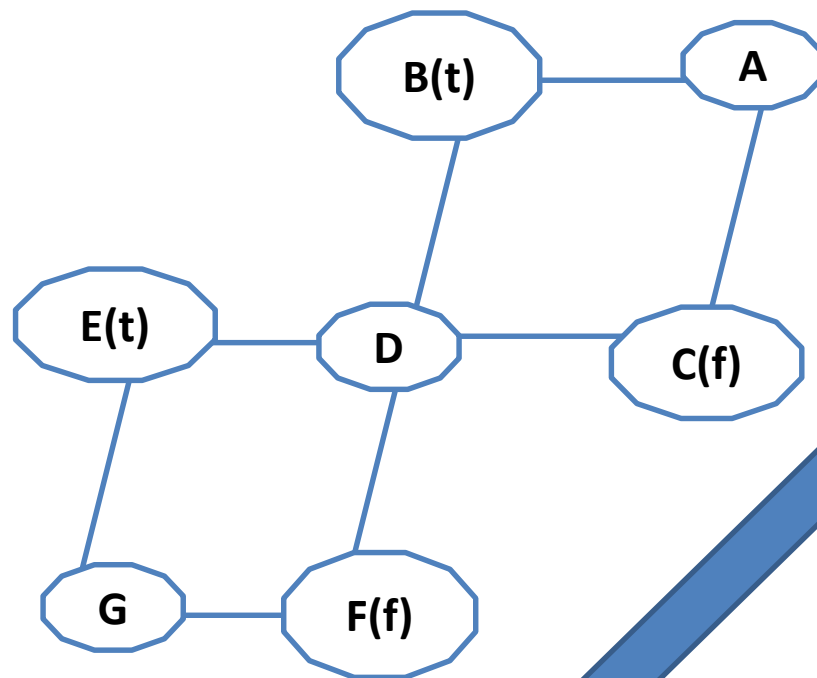
- Plugging this into a SAT Solver would render the following assignment of variables for the formula to be satisfied:
- $a0 = \text{True} (1)$ $a1 = \text{True} (1)$ $a2 = \text{False} (0)$ $a3 = \text{False} (0)$ $a4 = \text{True} (1)$ $a5 = \text{False} (0)$ $a6 = \text{False} (0)$ $a7 = \text{False} (0)$
- We got $00010011 = 19$

- Once the entire Software DNA Map is represented in this format of TRUES, FALSES, NOTS, ANDS, and
- ORS, a wide variety of formulas can be constructed from this representation and SAT solvers can be applied to analyze the code for additional, more sophisticated quality and security problems. It is this bit-accurate representation of the software that enables more precise static analysis than previously was possible based solely on path simulation.

- There are clearly four paths through this code base (a-b-d-e-g, a-c-d-e-g, a-b-d-f-g, a-c-d-f-g).



- Let's assume we have the following expressions
- $[a]:if(x == 0)[d]:if(x != 0)$

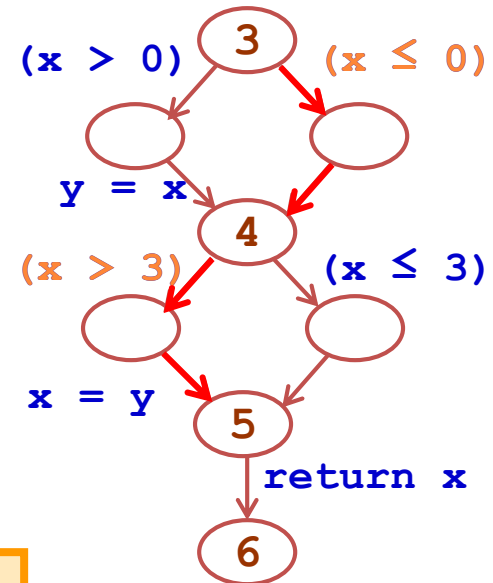


- The SAT solver see “ $x == 0 \text{ AND } x != 0$ ”
- The SAT solver says “this cannot be satisfied boolianly”
- while there might appear to be 4 paths through the control flow graph, we know that because of the dependency between the condition of (a) and condition of (d), there are only 2 paths through the code base.
- If the analysis decides to explore the path **a-b-d-e-g**, this would be The SAT solver see “ $x == 0 \text{ AND } x != 0$ ”
- **The SAT solver says “this cannot be satisfied boolianly”**
- while there might appear to be 4 paths through the control flow graph, we know that because of the dependency between the condition of (a) and condition of (d), there are only 2 paths through the code base. If the analysis decides to explore the path a-b-d-e-g, this would be a FALSE path because it’s impossible to execute at runtime. Moreover, if the analysis reported a defect on this path, that defect would clearly be a false positive since that path cannot exist when running the program.

- a FALSE path because it’s impossible to execute at runtime. Moreover, if the analysis reported a defect on this path, that defect would clearly be a false positive since that path cannot exist when running the program.

- **false error**: reported by analyzer but not in fact a latent error in program

```
1 int f(int x) {  
2   int y;  
3   if (x > 0) y = x;  
4   if (x > 3) x = y;  
5   return x;  
6 }
```



~~Warning 014:
Variable 'y' (line 2) may not have been initialized~~

```

f.c
1  #include <f.h>
2  void main (void)
3  f(1); //No Violation
4  Do something.....
5      f(-1); //Violation
6  }

```

```

f.h
1  int f(int x) {
2      int y;
3  if (x > 0) y = x;
4  else;
5  y++;
6  return x;
7  }

```



Typical Defect....

```
void buffer_size_example()
{
char dest[128]; char source[256]; strncpy(dest,
    source, strlen(source));
}
```

// This will flag an error as the size argument to strncpy() // can possibly be up to 255, yet the destination only has // room for 128 elements (127 chars and the null termination).

But it is never that obvious....

Buffer overrun

Or even looking remotely like that....

```
void func (char *passedStr)
{
char localStr[4];
strcpy(localStr, passedStr); // length of passedStr is not
checked
}
```

```
int main (int argc, char **argv)
{
func(argv[1]);
}
```


It can look like that..... History in the making
The code that made the iPhone what it is...

The *LIBTIFF VULNERABILITY*

```
static int
TIFFFetchShortPair(TIFF* tif, TIFFDirEntry* dir)
{
switch (dir->tdir_type) {
case TIFF_BYTE:
case TIFF_SBYTE:
{
uint8 v[4];
return TIFFFetchByteArray(tif, dir, v)
&& TIFFSetField(tif, dir->tdir_tag, v[0], v[1]);
}
case TIFF_SHORT:
case TIFF_SSHORT:
{
uint16 v[2];
return TIFFFetchShortArray(tif, dir, v)
&& TIFFSetField(tif, dir->tdir_tag, v[0], v[1]);
}
default:
return 0;
}
}
```



Live Demo!



The screenshot shows the 'Test Configurations' window in Microsoft Visual Studio. The window title is 'Test Configurations' and the name of the configuration is 'CATH_1_FDA C++ Phase 2'. The left pane shows a tree view of test configurations, including 'dotTEST', 'User-defined', 'Builtin', 'Team', and 'C++-test'. The right pane shows the 'Static Analysis' tab with the following settings:

- Enable Static Analysis:
- Limit maximum number of tasks reported per rule to: 1000
- Do not apply suppressions:
- Analyze files with parse errors:

The 'Rules Tree' tab is active, showing a list of rules. The 'Possible Bugs [BD-PB] - (10/11 enabled)' folder is expanded, and the following rules are checked:

- Avoid accessing arrays out of bounds [BD-PB-ARRAY - 1]
- Avoid use before initialization [BD-PB-NOTINIT - 1]
- Avoid null pointer dereferencing [BD-PB-NP - 1]
- Avoid buffer overflow due to defining incorrect format limits [BD-PB-BOF - 1]
- Avoid overflow due to reading a not zero terminated string [BD-PB-STR - 1]
- Avoid overflow when reading from a buffer [BD-PB-OVERFRD - 1]
- Avoid overflow when writing to a buffer [BD-PB-OVERFWR - 1]
- Avoid division by zero [BD-PB-ZERO - 1]
- Do not check for null after dereferencing [BD-PB-DEREF - 2]

The bottom status bar shows 'Error List | Quality Tasks | Test Progress | Coverage | Output | Find Results 1'. The bottom right corner of the window shows 'Ln 9 | Col 16 | Ch 16'.

תודה על ההקשבה

דני לייזרוביץ'